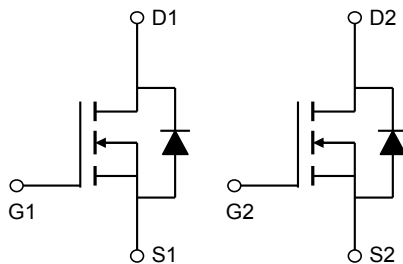
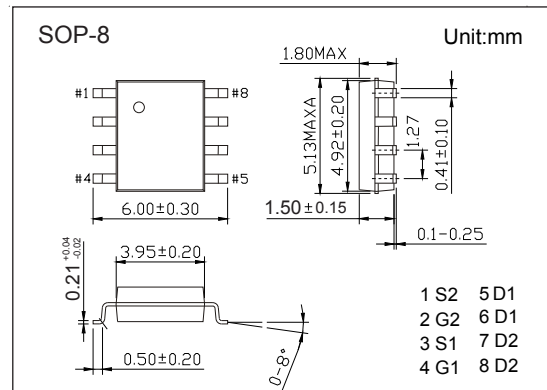


DUAL N-CANNEL MOSFET FOR SWITCHING

■ Features

- $V_{DS} = 30V$
- $I_D = 6A$ ($V_{GS} = 10V$)
- $R_{DS(ON)} < 20m\Omega$ ($V_{GS} = 10V$)
- $R_{DS(ON)} < 30m\Omega$ ($V_{GS} = 4.5V$)



■ Absolute Maximum Ratings $T_a = 25^\circ C$

| Parameter | Symbol | Rating | Unit | |
|---|------------------|------------------|------------|--------------|
| Drain-Source Voltage | V_{DS} | 30 | V | |
| Gate-Source Voltage | V_{GS} | ± 20 | | |
| Continuous Drain Current | I_D | $T_A=25^\circ C$ | 6 | A |
| | | $T_A=70^\circ C$ | 5 | |
| Pulsed Drain Current | I_{DM} | 30 | | |
| Avalanche Current | I_{AS}, I_{AR} | 10 | | |
| Avalanche Energy | $L=0.1mH$ | E_{AS}, E_{AR} | 5 | mJ |
| Power Dissipation | P_D | $T_A=25^\circ C$ | 2 | W |
| | | $T_A=70^\circ C$ | 1.3 | |
| Thermal Resistance.Junction- to-Ambient | R_{thJA} | $t \le 10s$ | 62.5 | $^\circ C/W$ |
| | | Steady-State | 90 | |
| Thermal Resistance.Junction- to-Lead | R_{thJL} | 40 | | |
| Junction Temperature | T_J | 150 | $^\circ C$ | |
| Storage Temperature Range | T_{stg} | -55 to 150 | | |

■ Marking

| | |
|---------|------|
| Marking | 4822 |
|---------|------|

■ Electrical Characteristics Ta = 25°C

| Parameter | Symbol | Test Conditions | Min | Typ | Max | Unit | |
|---------------------------------------|---------------------|---|-----------------|------|------|------|--|
| Drain-Source Breakdown Voltage | V _{DSS} | I _D =250 μA, V _{GS} =0V | 30 | | | V | |
| Zero Gate Voltage Drain Current | I _{DSS} | V _{DS} =30V, V _{GS} =0V | | | 1 | μA | |
| | | V _{DS} =30V, V _{GS} =0V, T _J =55°C | | | 5 | | |
| Gate-Body Leakage Current | I _{GSS} | V _{DS} =0V, V _{GS} =±20V | | | ±100 | nA | |
| Gate Threshold Voltage | V _{GS(th)} | V _{DS} =V _{GS} , I _D =250μA | 1.2 | | 2.4 | V | |
| Static Drain-Source On-Resistance | R _{DS(on)} | V _{GS} =10V, I _D =6A | | | 20 | mΩ | |
| | | V _{GS} =10V, I _D =6A T _J =125°C | | | 28 | | |
| | | V _{GS} =4.5V, I _D =5A | | | 32 | | |
| On State Drain Current | I _{D(ON)} | V _{GS} =10V, V _{DS} =5V | 30 | | | A | |
| Forward Transconductance | g _{FS} | V _{DS} =5V, I _D =6A | | 15 | | S | |
| Input Capacitance | C _{iss} | V _{GS} =0V, V _{DS} =15V, f=1MHz | | 255 | 310 | pF | |
| Output Capacitance | C _{oss} | | | 45 | | | |
| Reverse Transfer Capacitance | C _{rss} | | | 35 | 50 | | |
| Gate Resistance | R _g | V _{GS} =0V, V _{DS} =0V, f=1MHz | 1.6 | | 4.9 | Ω | |
| Total Gate Charge (10V) | Q _g | V _{GS} =10V, V _{DS} =15V, I _D =6A | | 5.2 | 6.3 | nC | |
| Total Gate Charge (4.5V) | | | | 2.55 | 3.2 | | |
| Gate Source Charge | | | Q _{gs} | | 0.85 | | |
| Gate Drain Charge | | | Q _{gd} | | 1.3 | | |
| Turn-On DelayTime | t _{d(on)} | V _{GS} =10V, V _{DS} =15V, R _L =2.5Ω, R _{GEN} =3Ω | | 4.5 | | ns | |
| Turn-On Rise Time | t _r | | | 2.5 | | | |
| Turn-Off DelayTime | t _{d(off)} | | | 14.5 | | | |
| Turn-Off Fall Time | t _f | | | 3.5 | | | |
| Body Diode Reverse Recovery Time | t _{rr} | I _F = 6A, di/dt= 100A/us | | 8.5 | | nC | |
| Body Diode Reverse Recovery Charge | Q _{rr} | | | 2.2 | | | |
| Maximum Body-Diode Continuous Current | I _S | | | | 2.5 | A | |
| Diode Forward Voltage | V _{SD} | I _S =1A, V _{GS} =0V | | | 1 | V | |

Note. The static characteristics in Figures 1 to 6 are obtained using <300us pulses, duty cycle 0.5% max.

■ Typical Characteristics

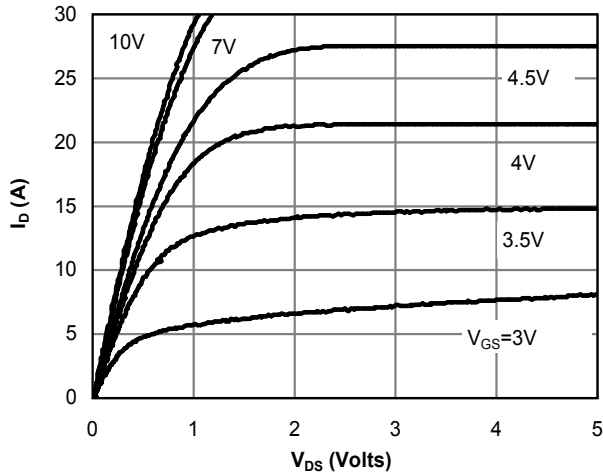


Fig 1: On-Region Characteristics (Note E)

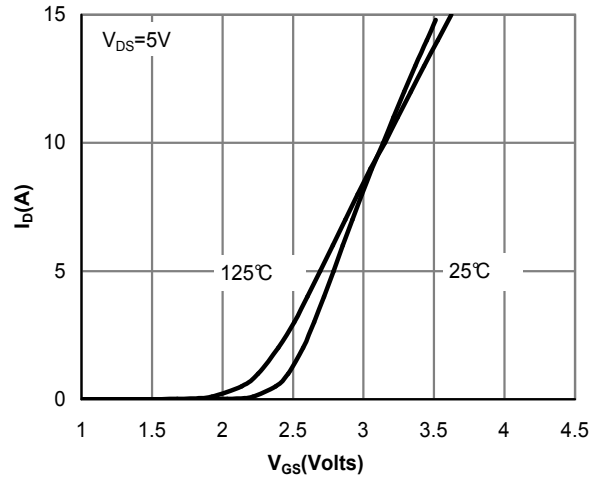


Figure 2: Transfer Characteristics (Note E)

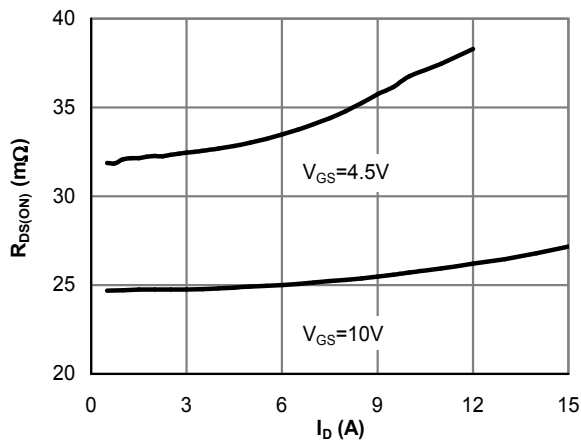


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

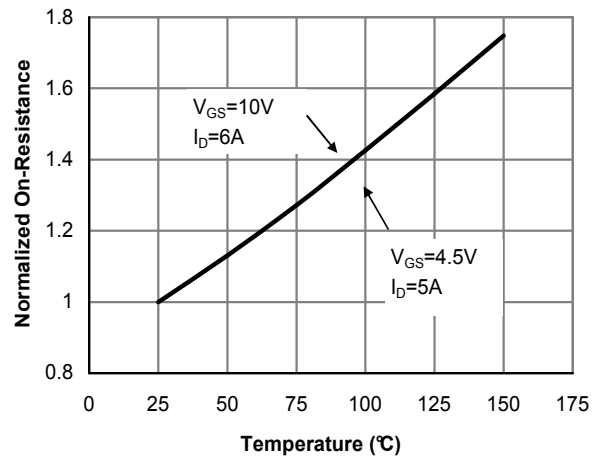


Figure 4: On-Resistance vs. Junction Temperature

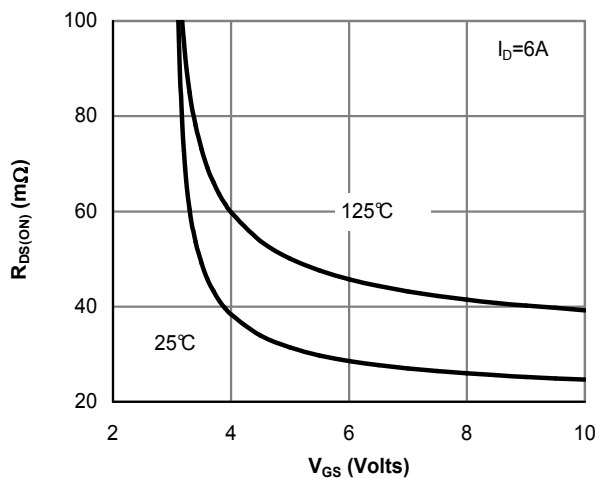


Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

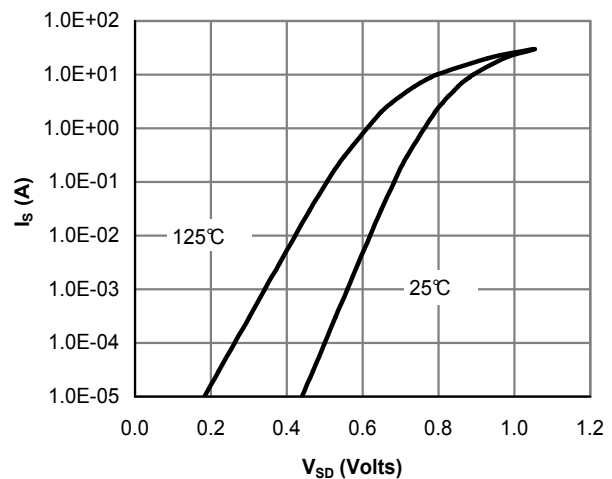


Figure 6: Body-Diode Characteristics (Note E)

■ Typical Characteristics

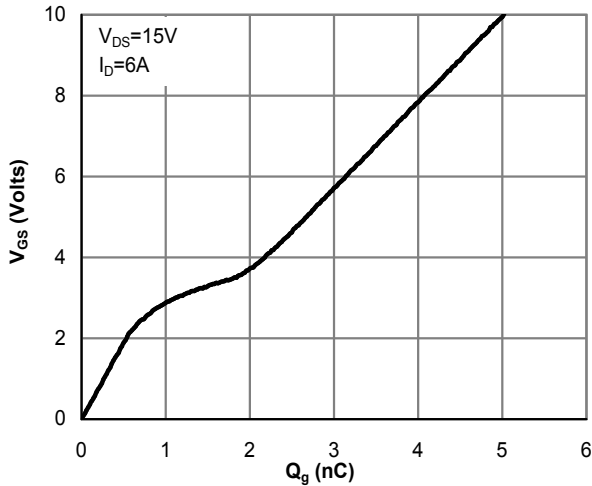


Figure 7: Gate-Charge Characteristics

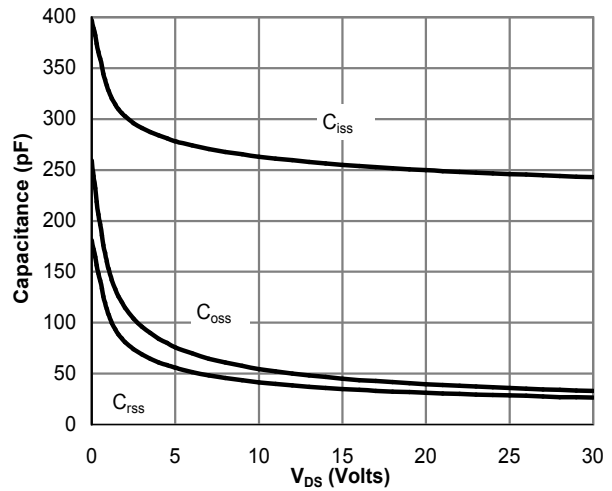


Figure 8: Capacitance Characteristics

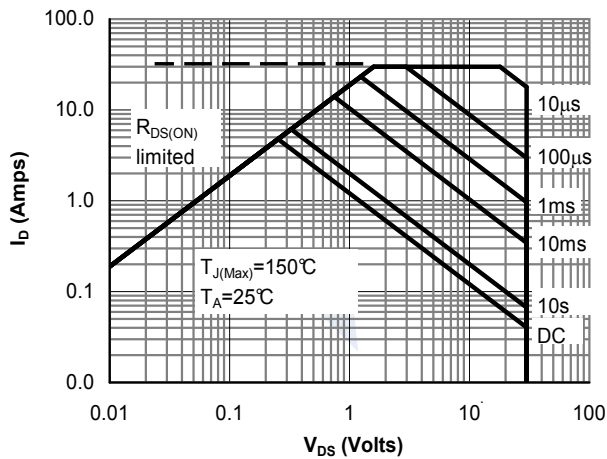


Figure 10: Maximum Forward Biased Safe Operating Area (Note F)

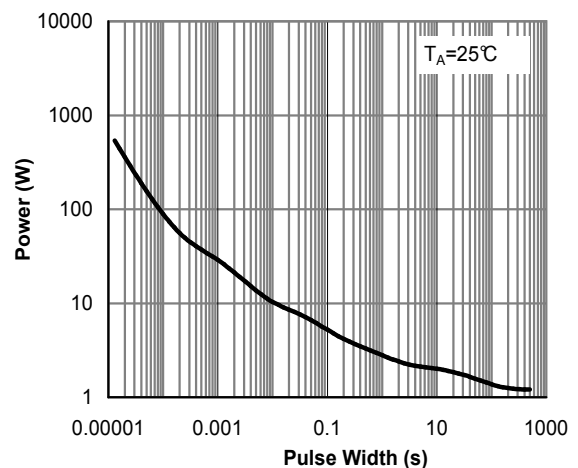


Figure 11: Single Pulse Power Rating Junction-to-Ambient (Note F)

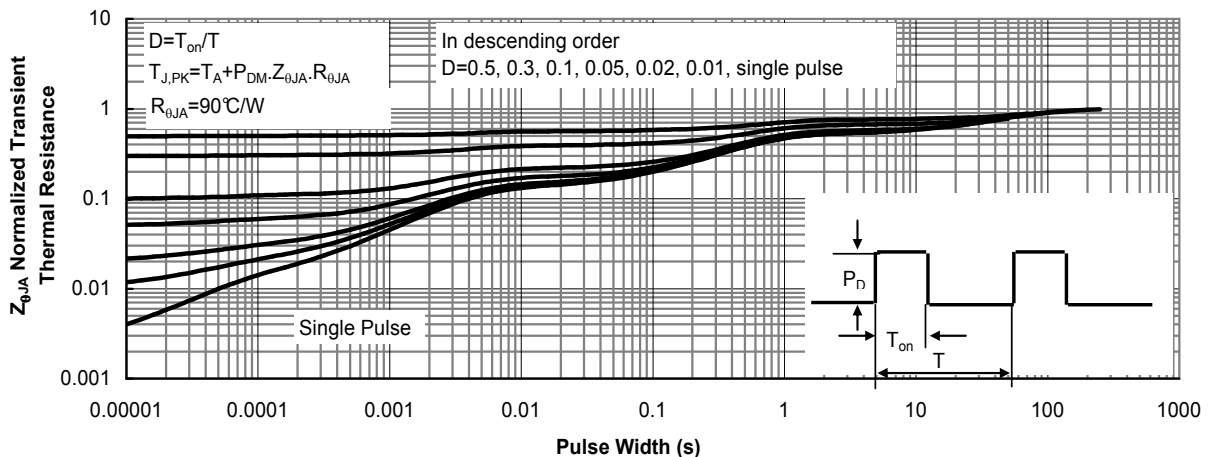


Figure 12: Normalized Maximum Transient Thermal Impedance (Note F)