

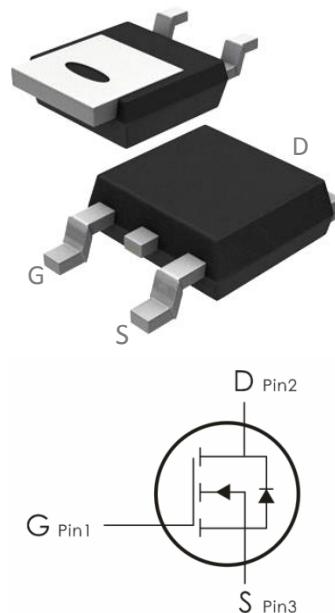
Description:

This N-Channel MOSFET uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge.

It can be used in a wide variety of applications.

Features:

- 1) $V_{DS}=20V, I_D=60A, R_{DS(ON)}<6m\Omega @ V_{GS}=4.5V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra $R_{DS(ON)}$.
- 5) Excellent package for good heat dissipation.



Absolute Maximum Ratings: ($T_C=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	20	V
V_{GS}	Gate-Source Voltage	± 12	V
I_D	Continuous Drain Current- $T_C=25^\circ C$	60	A
	Continuous Drain Current- $T_C=100^\circ C$	42	
	Pulsed Drain Current ¹	210	
E_{AS}	Single Pulse Avalanche Energy	200	mJ
P_D	Power Dissipation, $T_C=25^\circ C$	60	W
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to +150	$^\circ C$

Thermal Characteristics:

Symbol	Parameter	Max	Units
R_{eJC}	Thermal Resistance,Junction to Case	2.1	$^\circ C/W$
R_{eJA}	Thermal Resistance,Junction to Ambient	---	

Electrical Characteristics: ($T_C=25^\circ\text{C}$ unless otherwise noted)

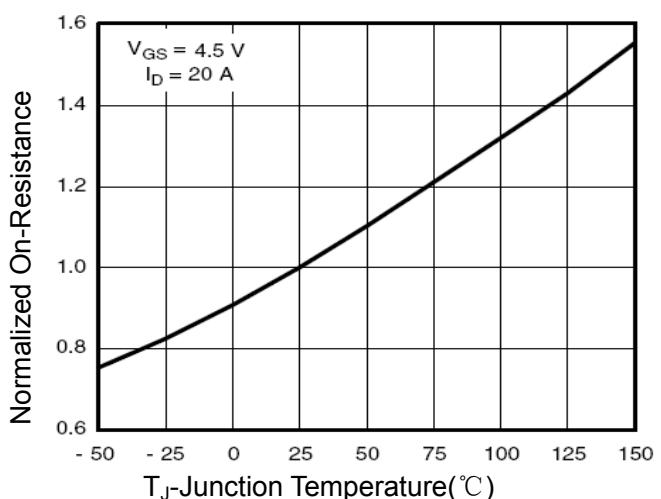
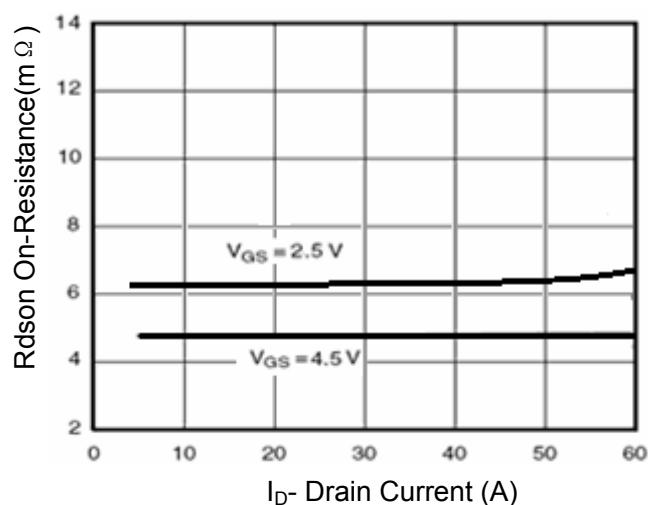
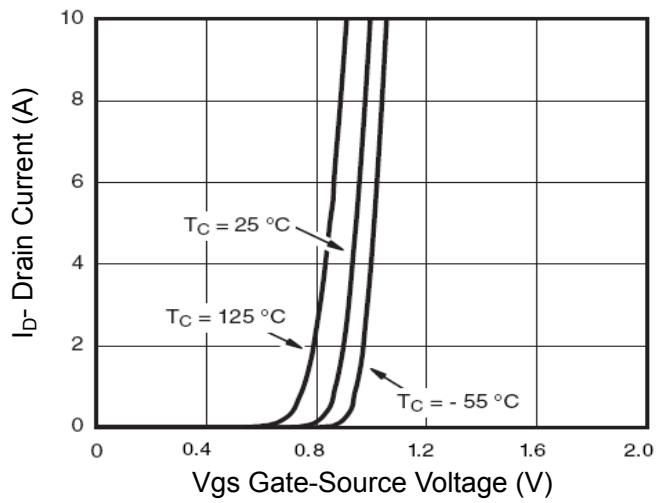
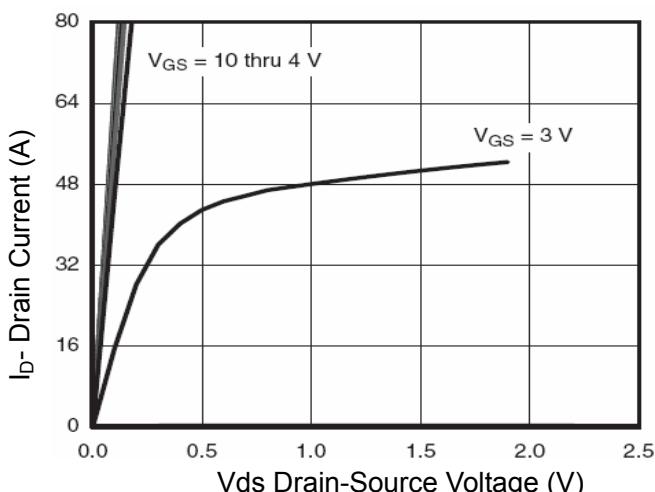
Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
$\mathbf{BV_{DSS}}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}, I_D=250 \mu\text{A}$	20	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{GS}=0\text{V}, V_{DS}=20\text{V}$	---	---	1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}, V_{DS}=0\text{A}$	---	---	± 100	nA
On Characteristics						
$V_{GS(\text{th})}$	GATE-Source Threshold Voltage	$V_{GS}=V_{DS}, I_D=250 \mu\text{A}$	0.5	0.75	1.0	V
$R_{DS(\text{ON})}$	Drain-Source On Resistance ³	$V_{GS}=4.5\text{V}, I_D=20\text{A}$	---	4.8	6	$\text{m } \Omega$
		$V_{GS}=2.5\text{V}, I_D=15\text{A}$	---	6.2	9	
G_{FS}	Forward Transconductance	$V_{DS}=10\text{V}, I_D=20\text{A}$	15	---	---	S
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS}=10\text{V}, V_{GS}=0\text{V}, f=1\text{MHz}$	---	2000	---	pF
C_{oss}	Output Capacitance		---	500	---	
C_{rss}	Reverse Transfer Capacitance		---	200	---	
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time ³	$V_{DD}=10\text{V}, I_D=2\text{A}, V_{GS}=4.5\text{V}, R_G=3\Omega$	---	6.4	---	ns
t_r	Rise Time ^{2,3}		---	17.2	---	ns
$t_{d(off)}$	Turn-Off Delay Time		---	29.6	---	ns
t_f	Fall Time ^{2,3}		---	16.8	---	ns
Q_g	Total Gate Charge ³	$V_{GS}=10\text{V}, V_{DS}=10\text{V}, I_D=20\text{A}$	---	27	30	nC
Q_{gs}	Gate-Source Charge		---	6.5	---	nC
Q_{gd}	Gate-Drain "Miller" Charge		---	6.4	---	nC
Drain-Source Diode Characteristics						
V_{SD}	Source-Drain Diode Forward Voltage ³	$V_{GS}=0\text{V}, I_S=10\text{A}$	---	---	1.2	V

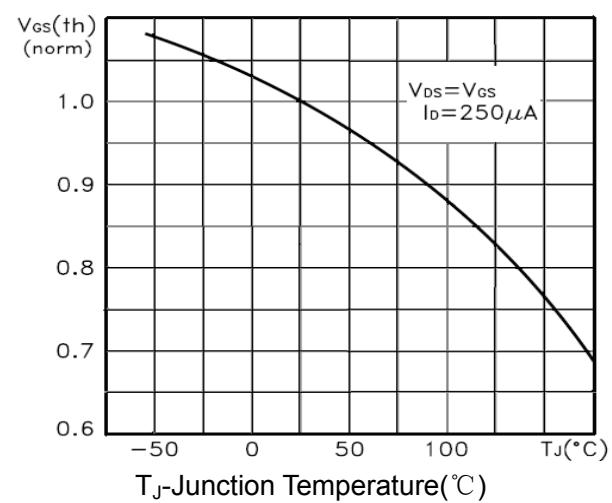
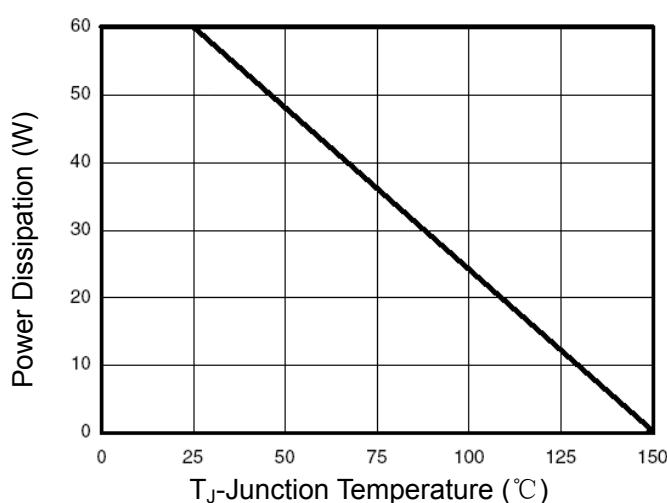
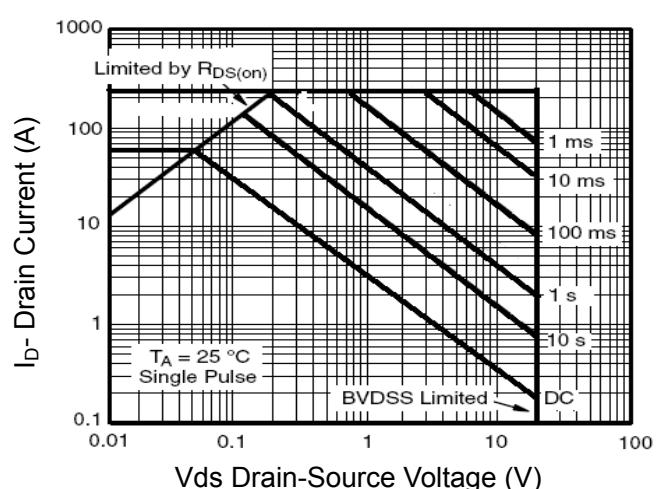
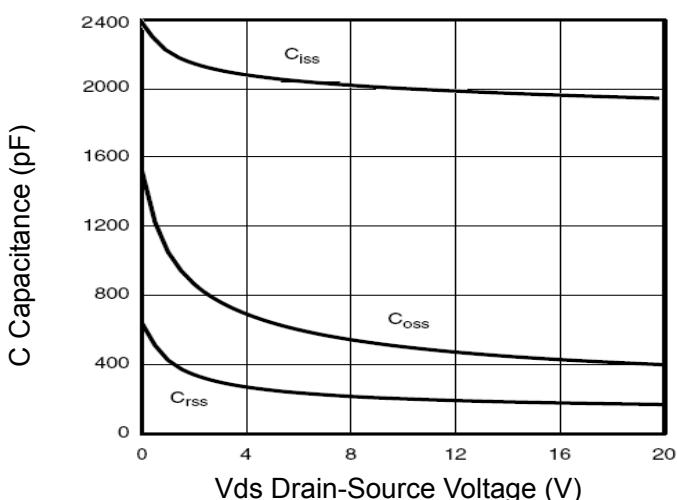
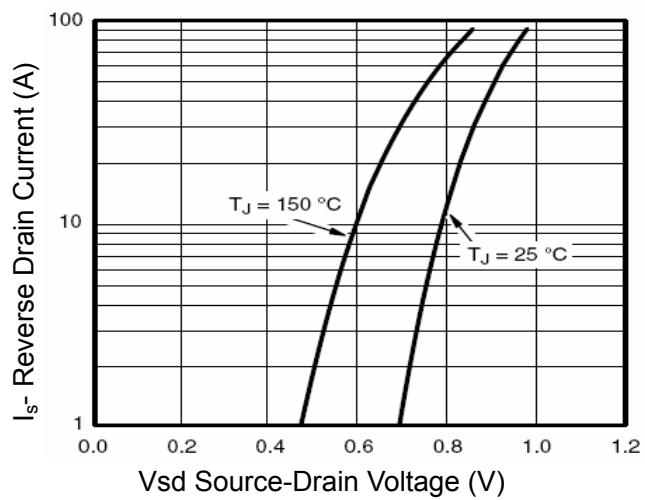
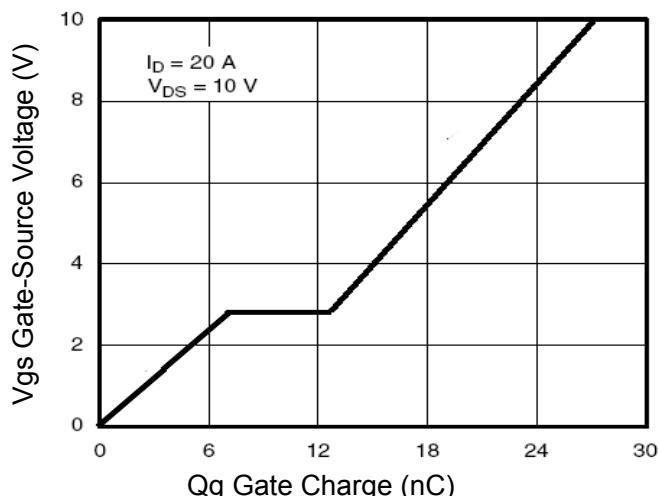
LS	Diode Forward Current (Note 2)		---	---	60	A
Trr	Reverse Recovery Time	$T_J = 25^\circ\text{C}$, IF = 20A $dI/dt = 100\text{A}/\mu\text{s}$ (Note 3)	---	25	---	Ns
Qrr	Reverse Recovery Charge		---	24	---	Nc

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production
5. EAS condition : $T_J=25^\circ\text{C}$, $V_{DD}=10\text{V}$, $V_G=10\text{V}$, $L=0.5\text{mH}$, $R_g=25\Omega$,

Typical Characteristics: ($T_c=25^\circ\text{C}$ unless otherwise noted)





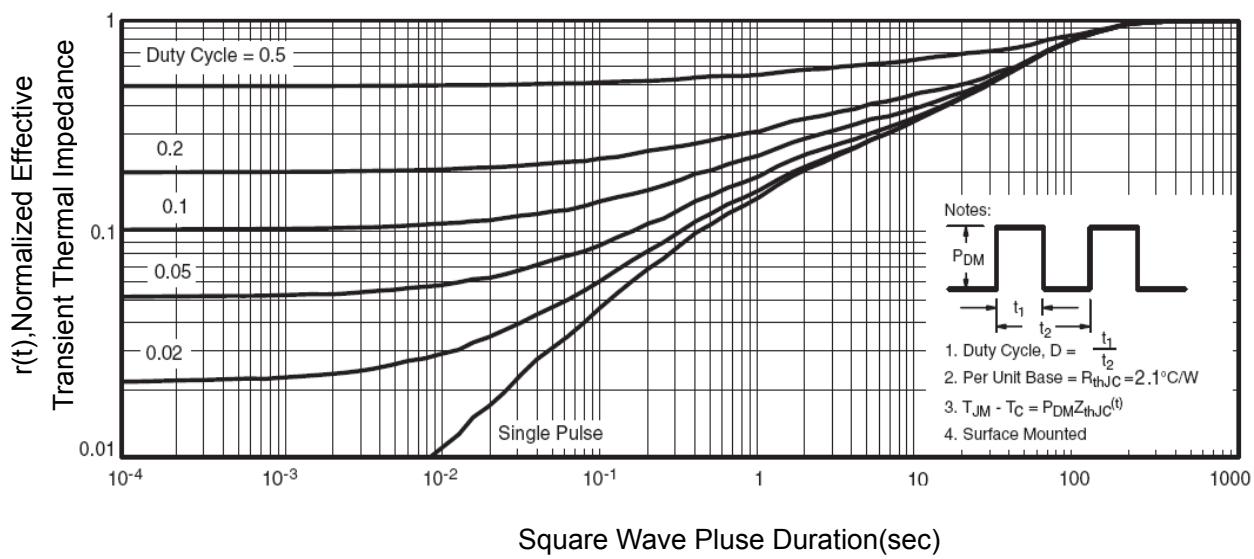


Figure 11 Normalized Maximum Transient Thermal Impedance