

P-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY

V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A)	Q_g (Typ.)
- 30	0.060 at $V_{GS} = - 10$ V	7.6 ^a	2 nC
	0.075 at $V_{GS} = - 4.5$ V	6 ^a	

FEATURES

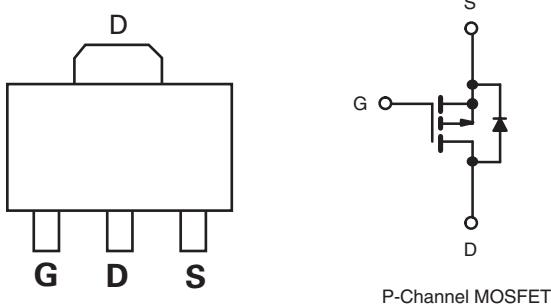
- Halogen-free
- TrenchFET® Power MOSFET
- 100 % R_g Tested



RoHS
COMPLIANT

APPLICATIONS

- DC/DC Converter
 - Load Switch
 - Adaptor Switch



ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless otherwise noted

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	- 30	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ($T_J = 150$ °C)	I_D	- 7.6 ^a	A
		- 5.8	
		- 6 ^{a, b, c}	
		- 5.2 ^{b, c}	
Pulsed Drain Current	I_{DM}	- 20	
Continuous Source-Drain Diode Current	I_S	- 5.3	W
		- 2.1 ^{b, c}	
Maximum Power Dissipation	P_D	6.3	W
		3.3	
		2.5 ^{b, c}	
		1.3 ^{b, c}	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 150	°C
Soldering Recommendations (Peak Temperature)		260	

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient	R_{thJA}	40	50	°C/W
Maximum Junction-to-Foot (Drain)	R_{thJF}	15	20	

Notes:

a. Package limited.

b. Surface Mounted on 1" x 1" FR4 board.

c. t = 5 s.

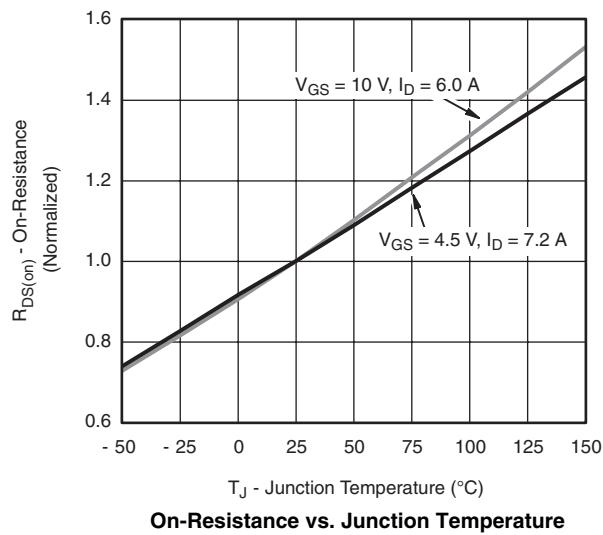
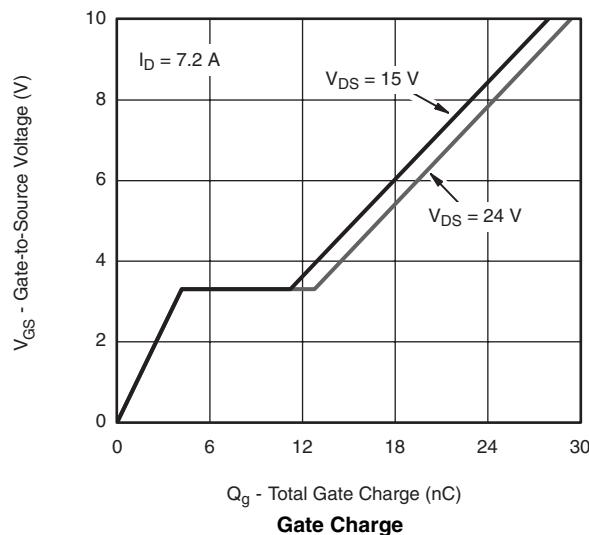
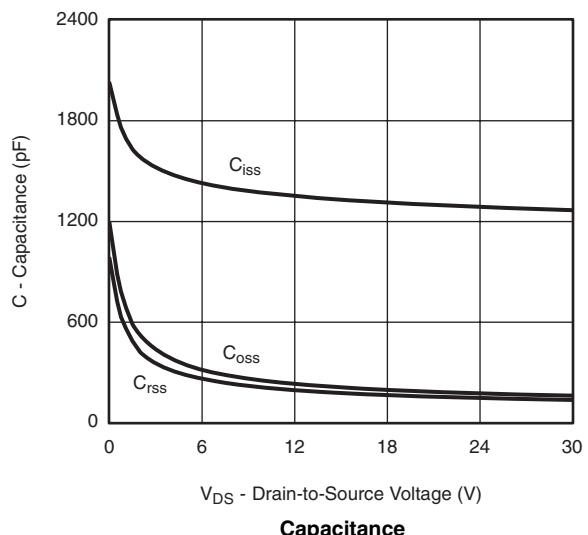
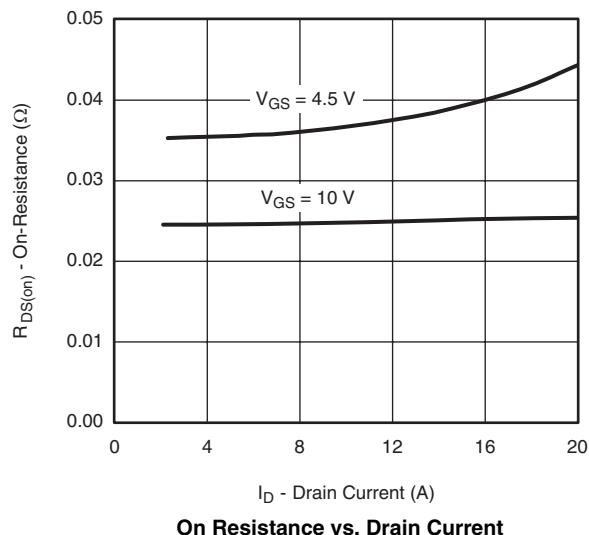
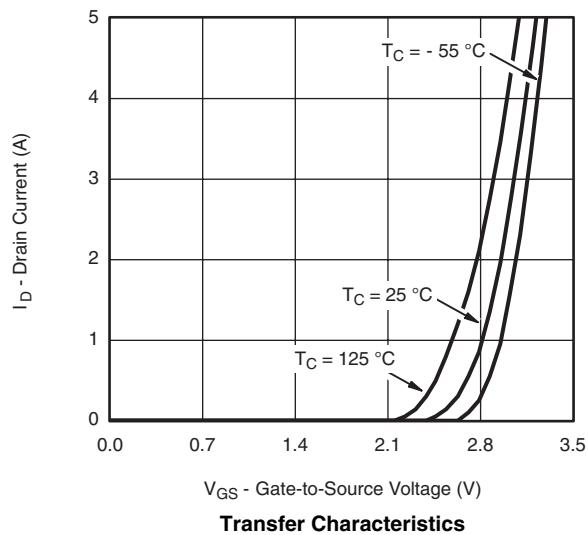
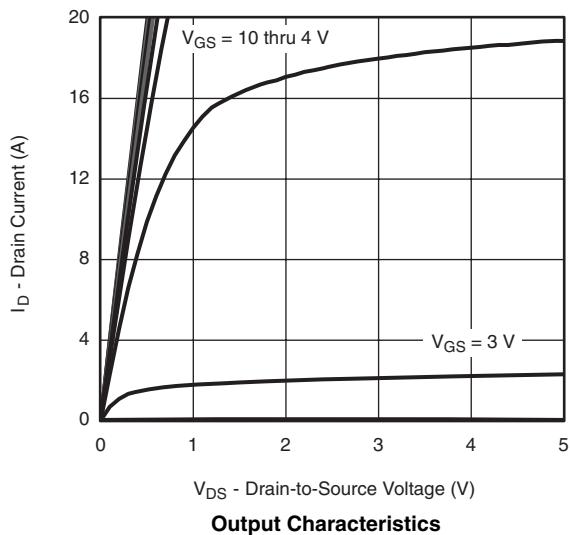
SPECIFICATIONS $T_J = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	-30			V
V_{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = -250 \mu\text{A}$		-30		$\text{mV/}^\circ\text{C}$
$V_{GS(\text{th})}$ Temperature Coefficient	$\Delta V_{GS(\text{th})}/T_J$			5		
Gate-Source Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = -250 \mu\text{A}$	-1		-3	V
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$			-1	μA
		$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 85^\circ\text{C}$			-5	
On-State Drain Current ^a	$I_{D(\text{on})}$	$V_{DS} \leq -5 \text{ V}, V_{GS} = -10 \text{ V}$	-20			A
Drain-Source On-State Resistance ^a	$R_{DS(\text{on})}$	$V_{GS} = -10 \text{ V}, I_D = -7.2 \text{ A}$		0.055	0.060	Ω
		$V_{GS} = -4.5 \text{ V}, I_D = -6.0 \text{ A}$		0.066	0.075	
Forward Transconductance ^a	g_{fs}	$V_{DS} = -15 \text{ V}, I_D = -7.2 \text{ A}$		18		S
Dynamic^b						
Input Capacitance	C_{iss}	$V_{DS} = -15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		1340		pF
Output Capacitance	C_{oss}			215		
Reverse Transfer Capacitance	C_{rss}			185		
Total Gate Charge	Q_g	$V_{DS} = -15 \text{ V}, V_{GS} = -10 \text{ V}, I_D = -7.2 \text{ A}$	28	42	nC	
Gate-Source Charge	Q_{gs}	$V_{DS} = -15 \text{ V}, V_{GS} = -4.5 \text{ V}, I_D = -7.2 \text{ A}$	15	23		
Gate-Drain Charge	Q_{gd}		4.5			
Gate Resistance	R_g		7.2			
Turn-On Delay Time	$t_{d(\text{on})}$	$V_{DD} = -15 \text{ V}, R_L = 2.6 \Omega$ $I_D \geq -5.8 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$	1.2	6	12	Ω
Rise Time	t_r			50	75	ns
Turn-Off Delay Time	$t_{d(\text{off})}$			140	210	
Fall Time	t_f			30	45	
Turn-On Delay Time	$t_{d(\text{on})}$			18	27	
Rise Time	t_r			11	17	
Turn-Off Delay Time	$t_{d(\text{off})}$			11	17	
Fall Time	t_f			37	56	
				12	18	
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I_S	$T_C = 25^\circ\text{C}$			-5.3	A
Pulse Diode Forward Current	I_{SM}				-20	
Body Diode Voltage	V_{SD}	$I_S = -5.8 \text{ A}, V_{GS} = 0 \text{ V}$		-0.8	-1.2	V
Body Diode Reverse Recovery Time	t_{rr}	$I_F = -5.8 \text{ A}, dI/dt = -100 \text{ A}/\mu\text{s}, T_J = 25^\circ\text{C}$		22	33	ns
Body Diode Reverse Recovery Charge	Q_{rr}			15	25	nC
Reverse Recovery Fall Time	t_a			13		ns
Reverse Recovery Rise Time	t_b			9		

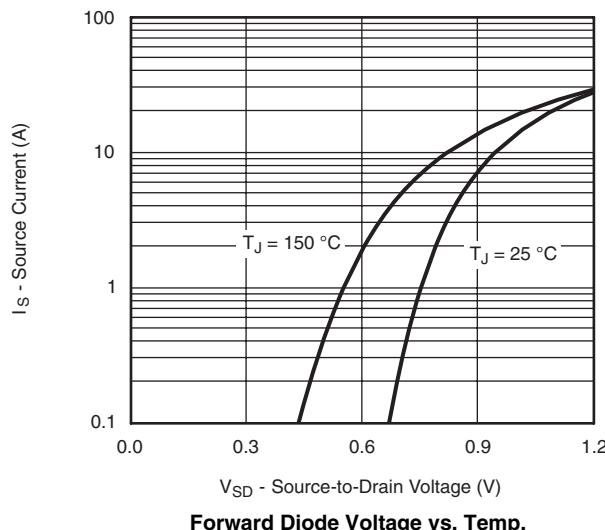
Notes:

- a. Pulse test; pulse width $\leq 300 \mu\text{s}$, duty cycle $\leq 2\%$.
b. Guaranteed by design, not subject to production testing.

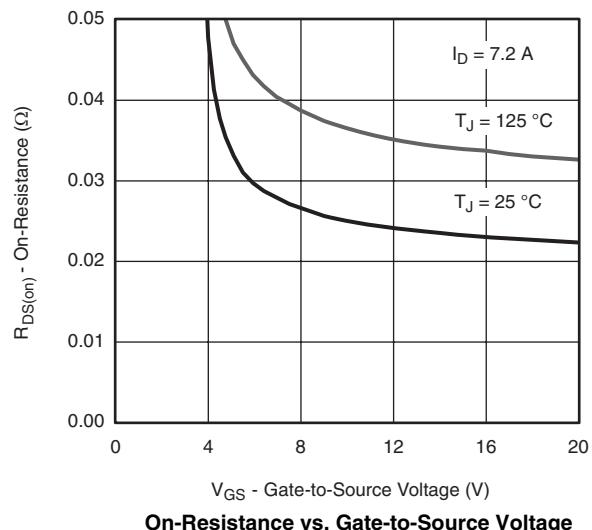
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

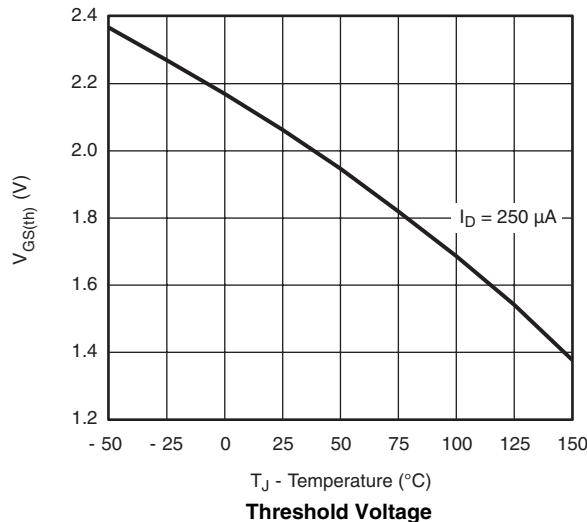
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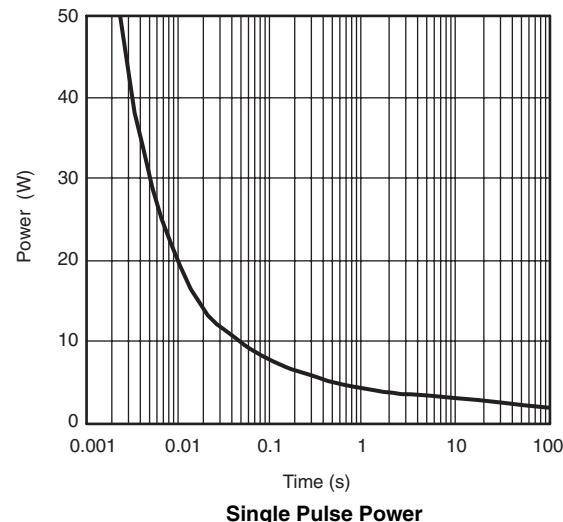
Forward Diode Voltage vs. Temp.



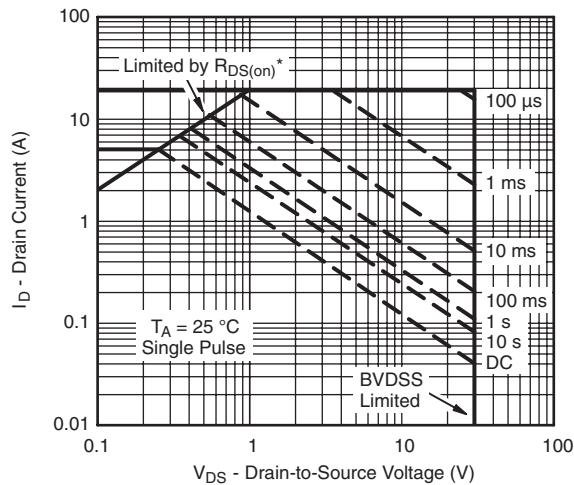
On-Resistance vs. Gate-to-Source Voltage



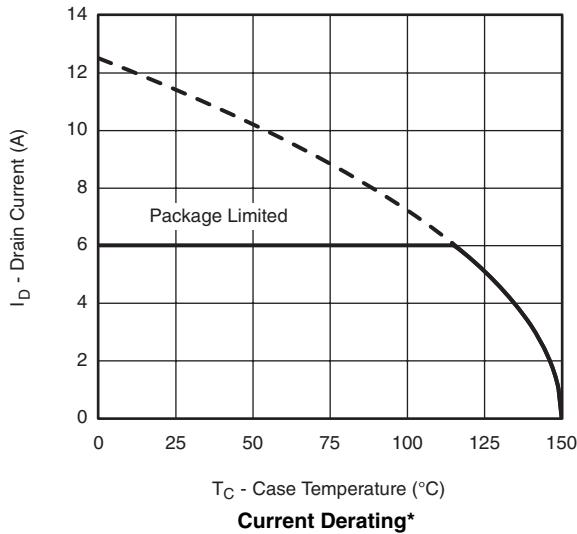
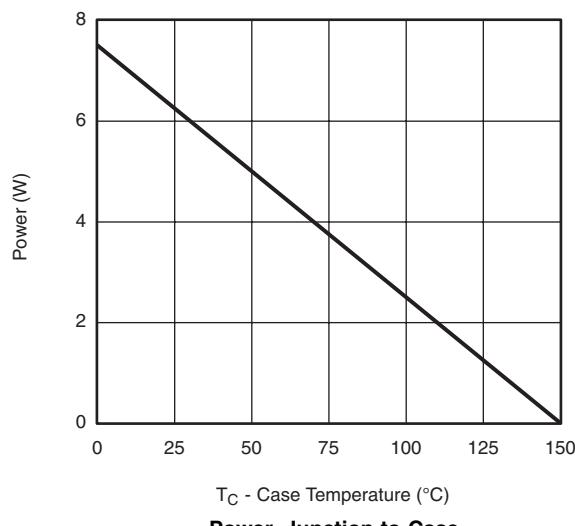
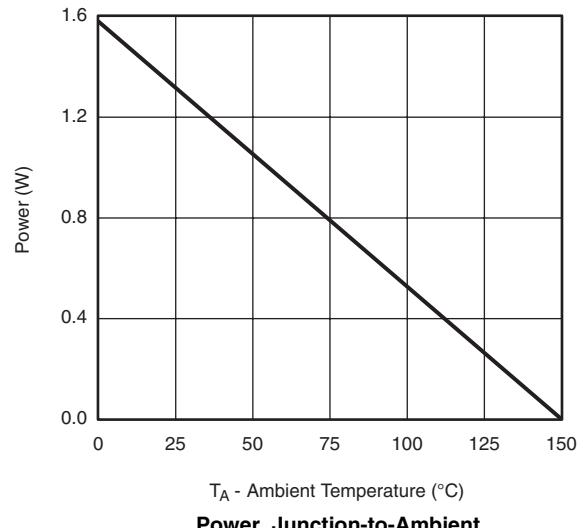
Threshold Voltage



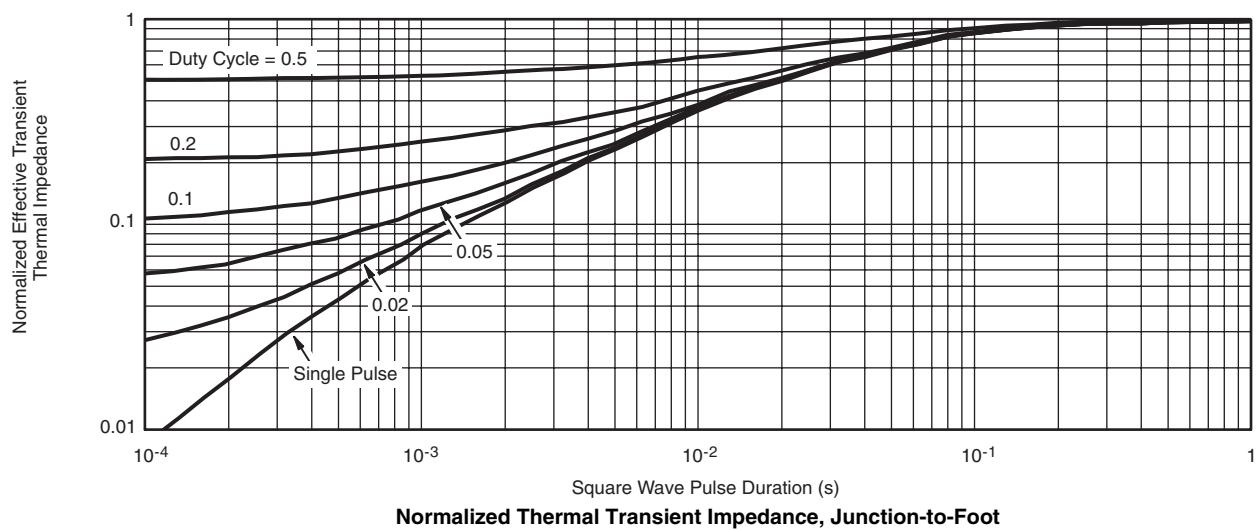
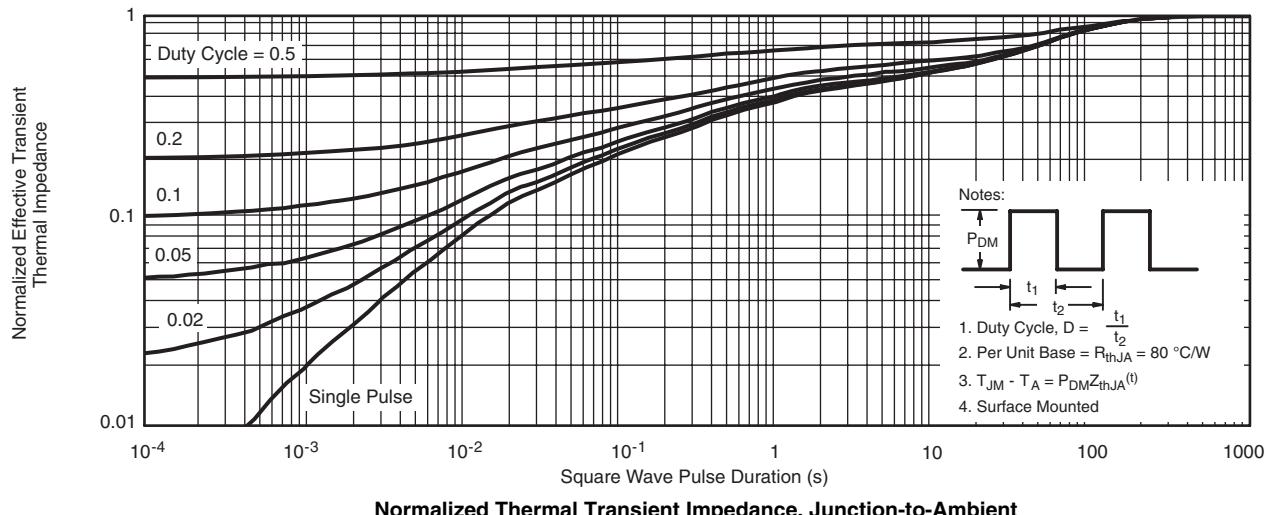
Single Pulse Power



* $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified
Safe Operating Area, Junction-to-Ambient

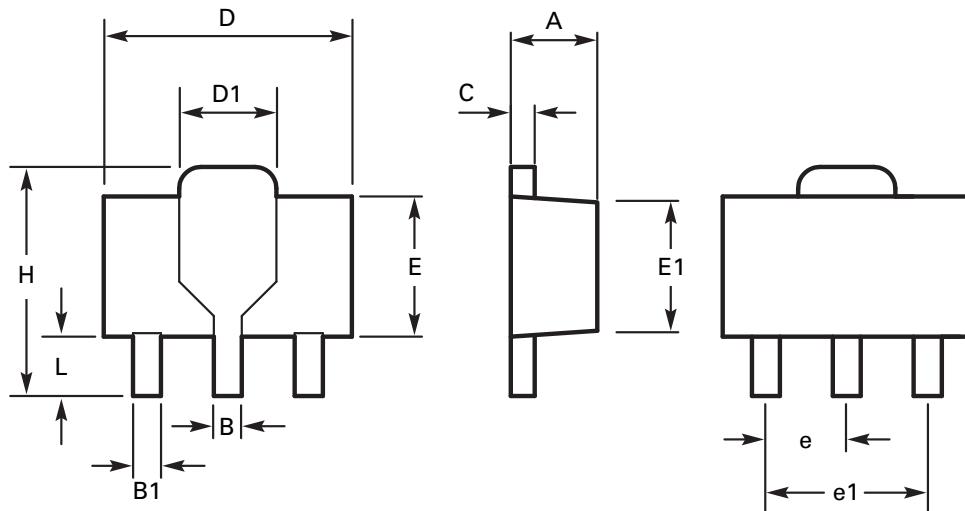
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted**Current Derating*****Power, Junction-to-Case****Power, Junction-to-Ambient**

* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

Package Information

Package outline - SOT89



DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min	Max	Min	Max		Min	Max	Min	Max
A	1.40	1.60	0.550	0.630	E	2.29	2.60	0.090	0.102
B	0.44	0.56	0.017	0.022	E1	2.13	2.29	0.084	0.090
B1	0.36	0.48	0.014	0.019	e	1.50 BSC		0.059 BSC	
C	0.35	0.44	0.014	0.017	e1	3.00 BSC		0.118 BSC	
D	4.40	4.60	0.173	0.181	H	3.94	4.25	0.155	0.167
D1	1.62	1.83	0.064	0.072	L	0.89	1.20	0.035	0.047

Note: Controlling dimensions are in millimeters. Approximate dimensions are provided in inches